## DECLARATION FOR PATENT APPLICATION AND POWER OF ATTORNEY

As a below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below adjacent to my name.

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of subject matter (process, machine, manufacture, or composition of matter, or an improvement thereof) which is claimed and for which a patent is sought by way of the application entitled

Integrated Circuit Package Including Interconnection Posts For Multiple Electrical Connections

was filed on as A	o. by the Preliminary Amendment attached hereto. Application Serial No. If on (if applicable).
was filed on as A	Application Serial No.

I hereby state that I have reviewed and understand the contents of the above identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose information, which is material to patentability as defined in Title 37, Code of Federal Regulations, § 1.56.

I hereby appoint the following practitioners to prosecute this application and to transact all business in the United States Patent and Trademark Office connected therewith:

**Customer Number 24251** 



Please address all correspondence and telephone calls to:

## James Parsons SKJERVEN MORRILL MacPHERSON LLP

25 Metro Drive, Suite 700 San Jose, California 95110-1349

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I declare that all statements made herein of my own knowledge are true, all statements made herein on information and belief are believed to be true, and all statements made herein are made with the knowledge that whoever, in any matter within the jurisdiction of the Patent and Trademark Office, knowingly and willfully falsifies, conceals, or covers up by any trick, scheme, or device a material fact, or makes any false, fictitious or fraudulent statements or representations, or makes or uses any false writing or document knowing the same to contain any false, fictitious or fraudulent statement or entry, shall be subject to the penalties including fine or imprisonment or both as set forth under 18 U.S.C. 1001, and that violations of this paragraph may jeopardize the validity of the application or

this document, or the validity or enforceability of any patent, trademark registration, or certificate resulting therefrom.

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796737 v1

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Integrated Circuit Package Including Interconnection Posts For Multiple Electrical Connections

which (check)	$\boxtimes$	is attached hereto.		
		and is amended by the Preliminary Amendment attached hereto.		
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